

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S14	9	330/10.ccls. and micro adj1 controller and @ad<="20050613"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/19 14:56
S15	8	375/238.ccls. and micro adj1 controller and @ad<="20050613"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/23 12:55
S17	463	330/10.ccls. and (PWM or (pulse adj1 width adj1 modulat\$3)) and @ad<="20030730"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/19 15:03
S18	340	330/207A,251.ccls. and (PWM or (pulse adj1 width adj1 modulat\$3)) and @ad<="20030730"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/19 15:03
S20	224	(330/10,207A,251.ccls. or 375/238. ccls.) and (PWM or (pulse adj1 width adj1 modulat\$3)) and (controller or microcontroller or microprocessor) and @ad<="20030730"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/19 17:13
S22	91	(330/10,207A,251.ccls. or 375/238. ccls.) and (noise near1 (suppress\$3 or reduc\$3)) and @ad<="20030730"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/19 17:50
S23	189	(330/10,207A,251.ccls. or 375/238. ccls.) and (PWM or (pulse adj1 width adj1 modulat\$3)) and flip near1 flop and @ad<="20030730"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/20 09:45
S25	25	(class near1 d) and ((micro adj1 processor) or (micro adj1 controller)) and PWM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/23 13:00

## EAST Search History

S27	2	330/10.ccls. and PWM with clock and (class near1 d) and ((micro adj1 processor) or (micro adj1 controller)) and @ad<="20030730"	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/23 13:16
S28	41	(pwm or (pulse near1 width near1 mudulat\$3)) same regulat\$3 and (second adj1 clock) and @aD<="20030730"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/05 15:14
S29	19	(pwm or (pulse near1 width near1 mudulat\$3)) same regulat\$3 and (second adj1 clock)	US-PGPUB	OR	ON	2007/06/05 15:14
S30	2	(pwm or (pulse near1 width near1 mudulat\$3)) same regulat\$3 same (second adj1 clock)	US-PGPUB	OR	ON	2007/06/05 15:14